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UNIVERSITY**



A Practical View on Baseband Processing Portability

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Agenda

→ Introduction

Portability versus Efficiency

Case Study

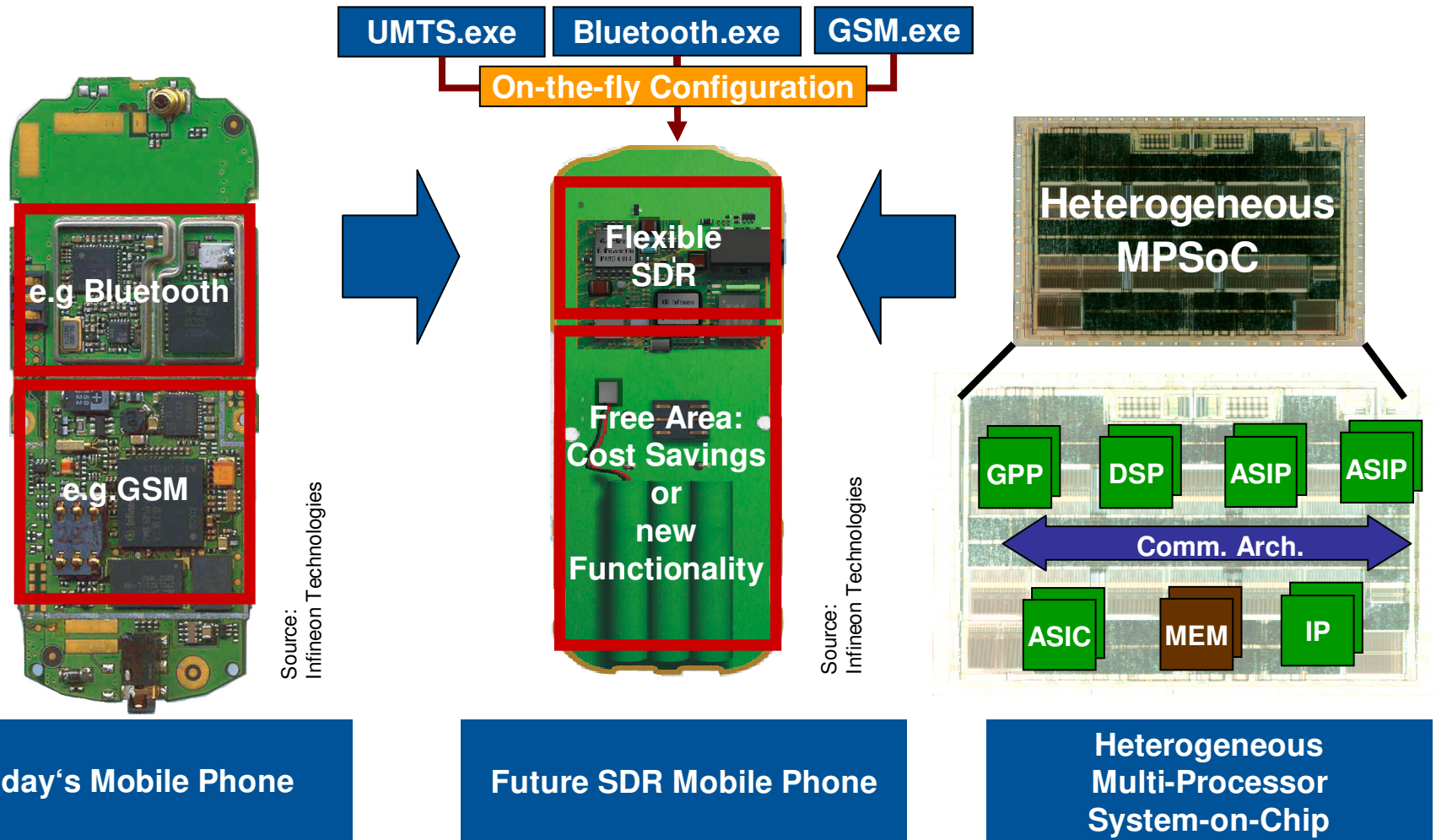
Measurements

In-Depth Analysis: FFT kernel

Conclusion/Outlook

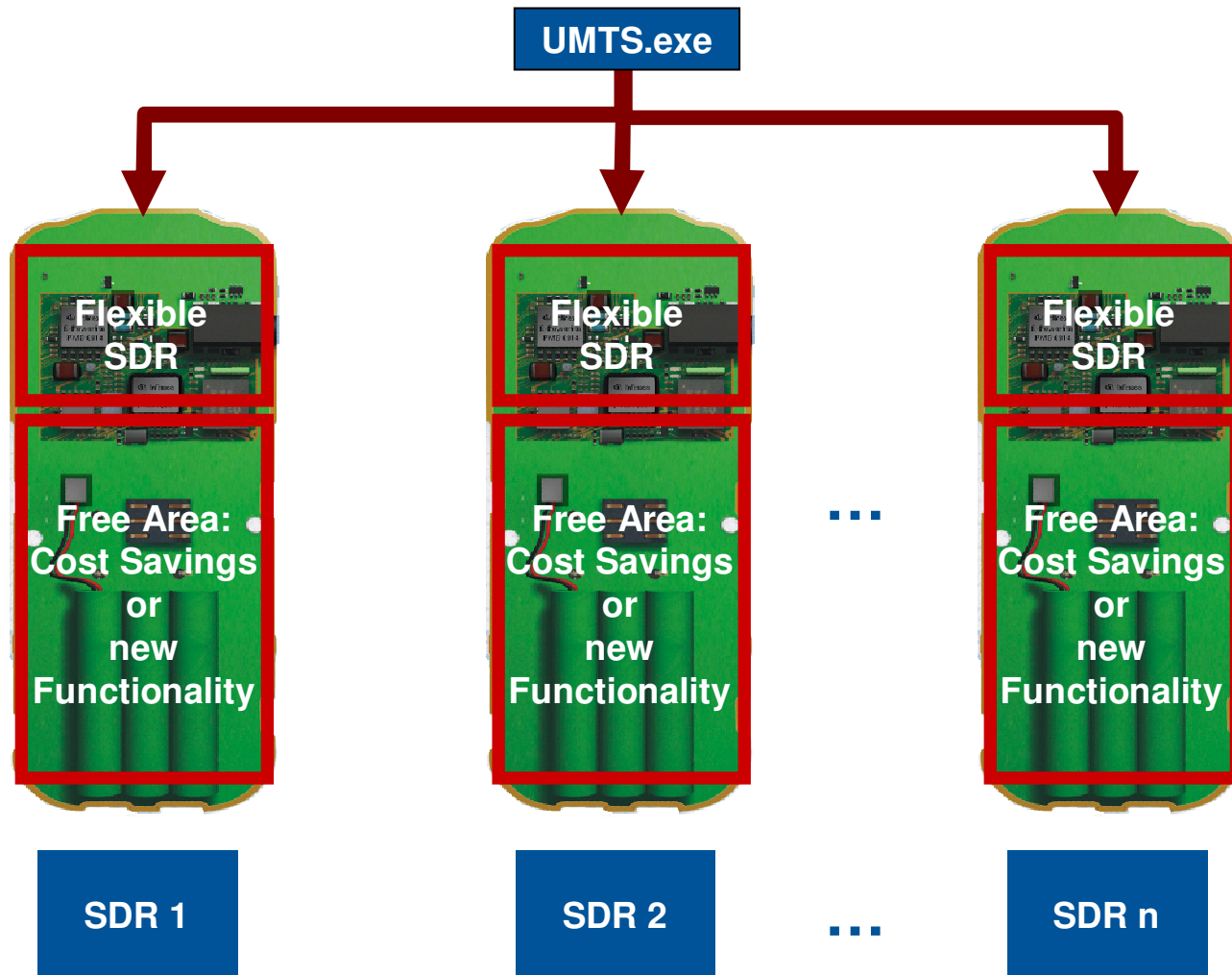
Motivation

- **SDR Design**
- **Waveform Development**



Motivation

- SDR Design
- **Waveform Development**



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SDR Community View

"Portable" Design

- Pure Software Solutions
- High Level Language Implementations

Hardware Designer's View

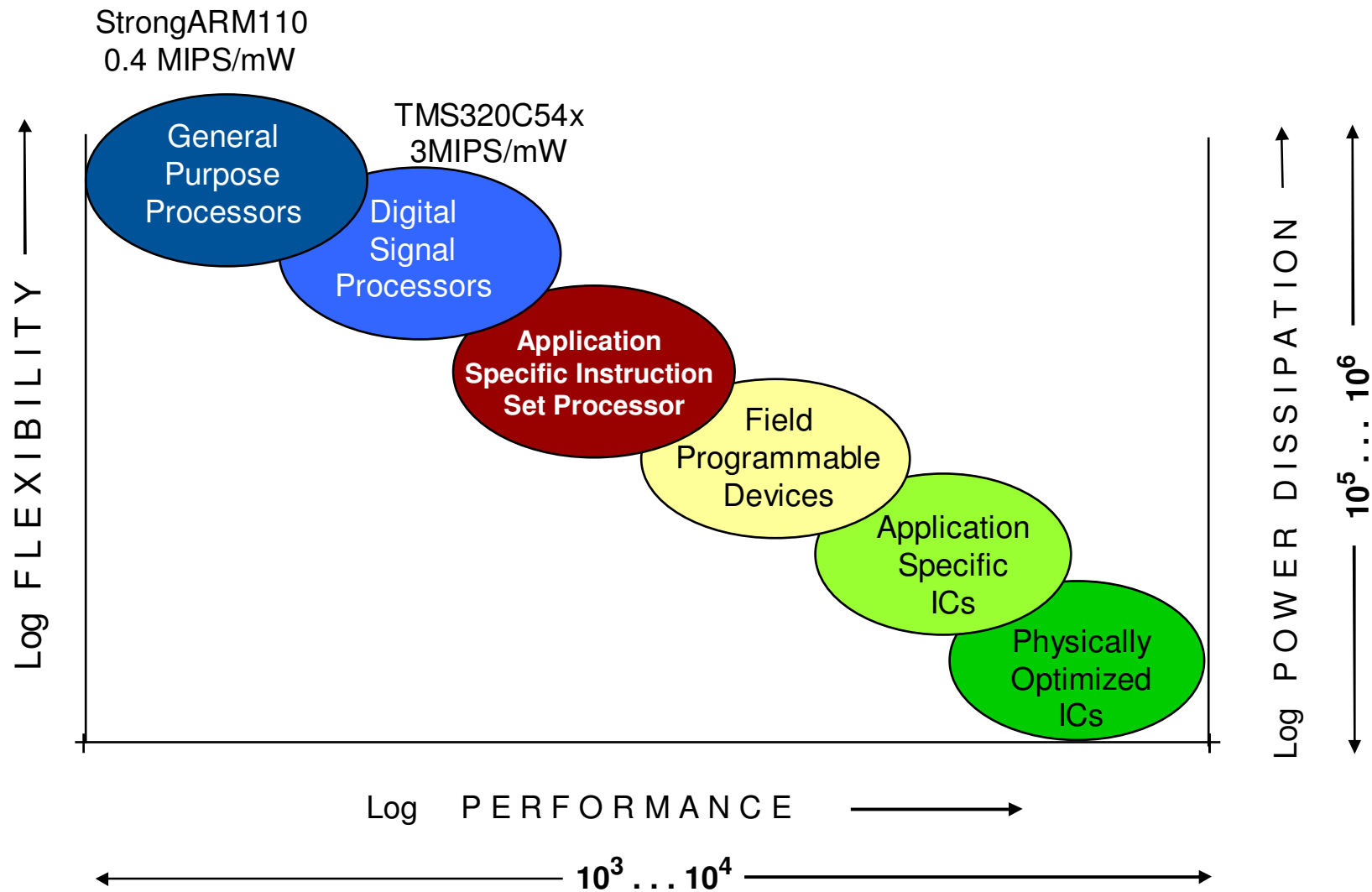
Maximum Efficiency

- Hardware Solution
- Add only minimum flexibility needed (e.g. ASIPs)



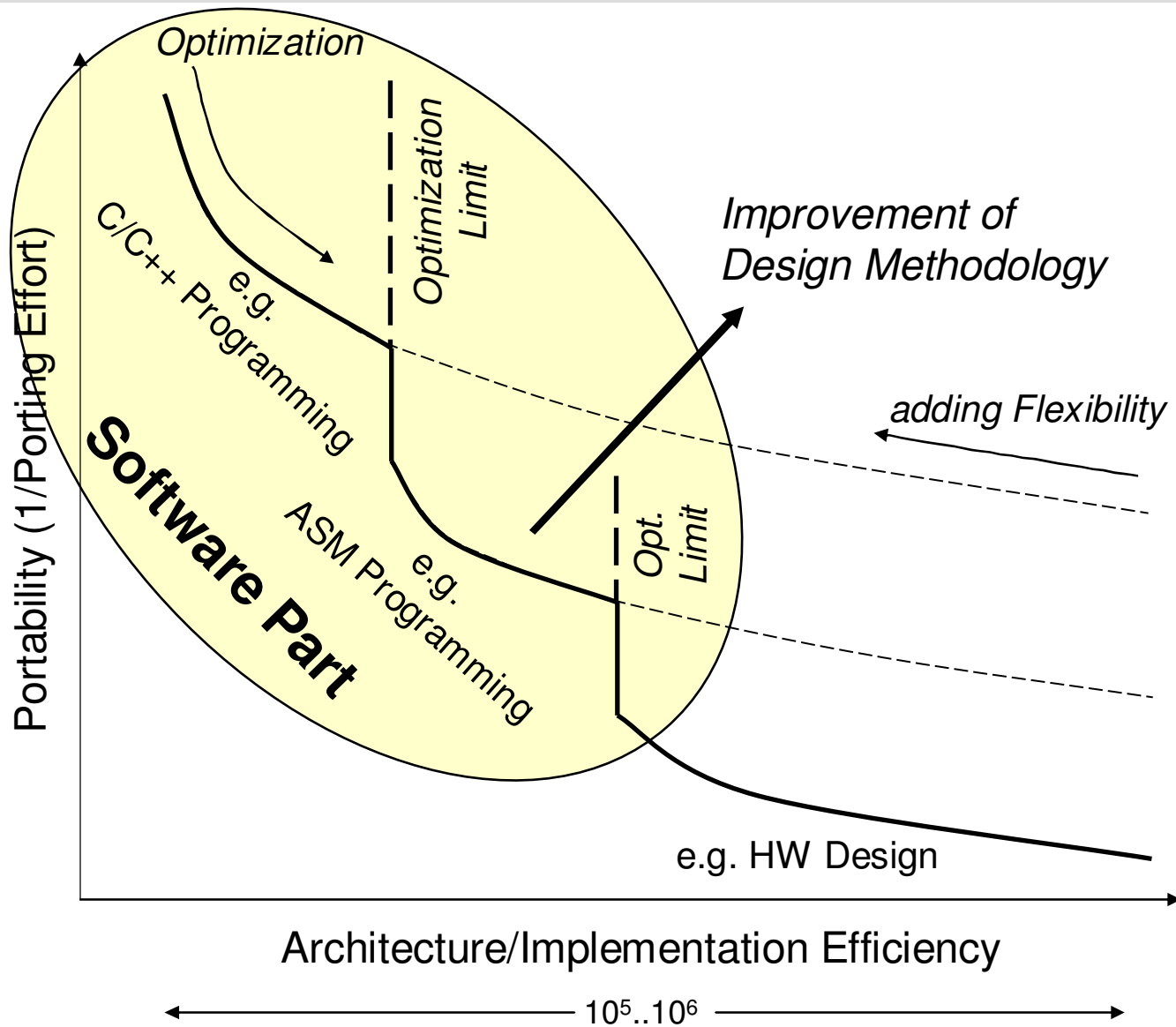
Cost Functions:

- Portability: e.g. Portability $\sim 1 / \text{Porting Effort}$
- Efficiency: e.g. Energy Efficiency $\sim \text{Bits} / \text{s} / \text{Watt}$



Source: T.Noll, RWTH Aachen University

The Efficiency vs. Portability Trade-Off



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Case Study: Portability vs. Efficiency

<u>Algorithmic kernel:</u>	X	<u>HW architecture:</u>	X	<u>Implementation Option:</u>
<ul style="list-style-type: none">▪ <i>Vector Operations</i>, e.g. addition, product, etc.▪ <i>Matrix operations</i>, e.g. transposition, etc.▪ <i>Filter operations</i>, e.g. FIR, adaptive LMS filter, etc.▪ <i>Correlation operations</i>, e.g. autocorrelation, etc.▪ <i>FFT operations</i>, e.g. radix-2 FFT		<ul style="list-style-type: none">▪ General Purpose Processors (GPPs):<ul style="list-style-type: none">▪ <i>ARM720T</i>▪ <i>ARM926EJ-S</i>▪ Digital Signal Processors (DSPs):<ul style="list-style-type: none">▪ <i>TI C55x</i>▪ <i>TI C64x</i>		<ul style="list-style-type: none">▪ <i>C-code</i>▪ <i>Optimized C-code</i> (compiler directives)▪ <i>Assembly code</i>

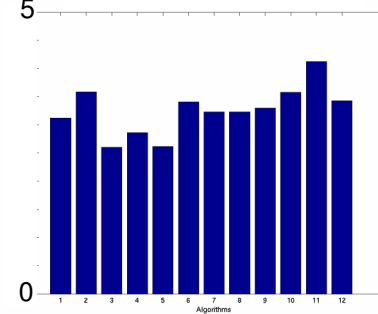
Measurement Results I: C-code implementations

Algorithms:

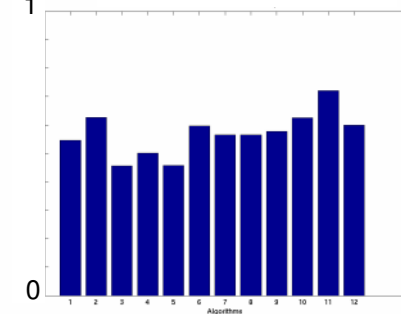
1. Vector Addition
2. Vector Product
3. Vector Max Value
4. Vector Max Index
5. Vector Sum Square
6. Matrix Multiplication
7. Matrix Transpose
8. Autocorrelation
9. FIR filter (generic)
10. Complex FIR filter
11. Adaptive LMS FIR filter
12. FFT (Radix-2)

ARM926EJ-S / C-code

Relative Speed-up

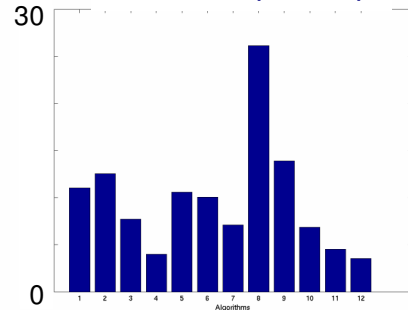


Relative Efficiency

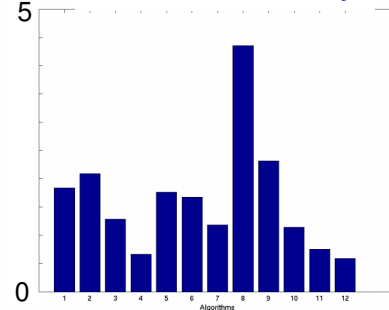


C55x / C-code

Relative Speed-up

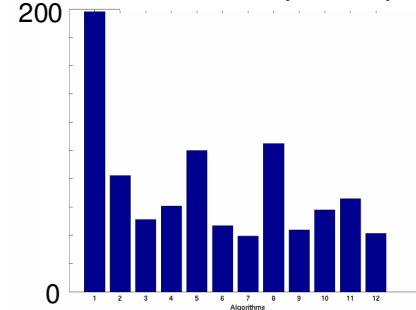


Relative Efficiency

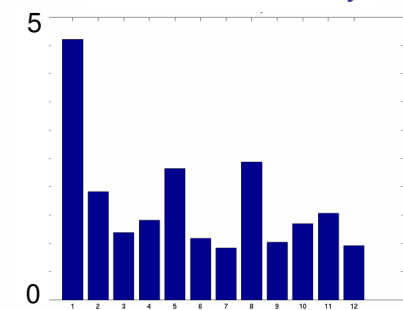


C64x / C-code

Relative Speed-up



Relative Efficiency



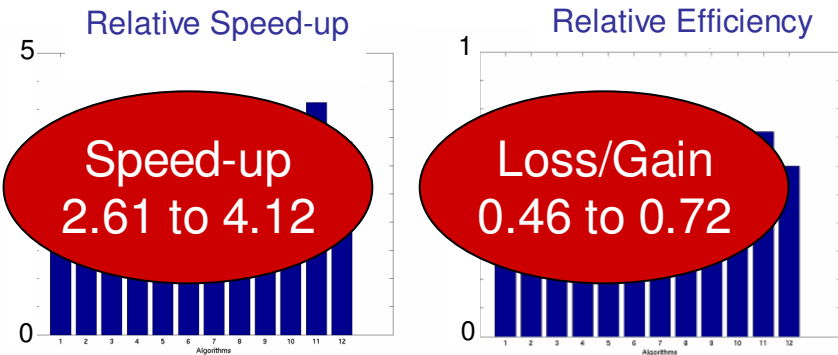
Note: Measurements are normed to ARM720T / C-code implementation

Measurement Results I: C-code implementations

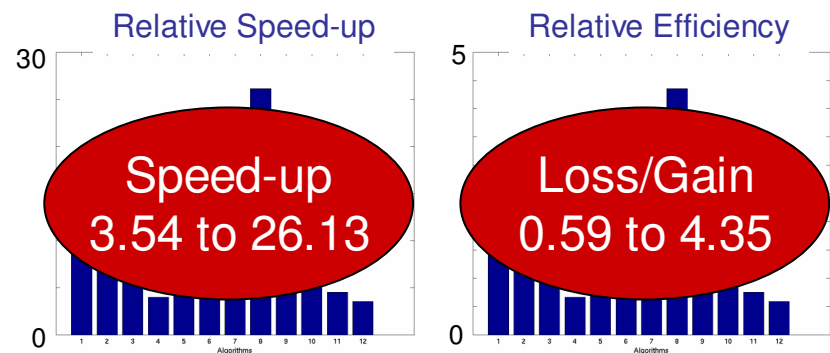
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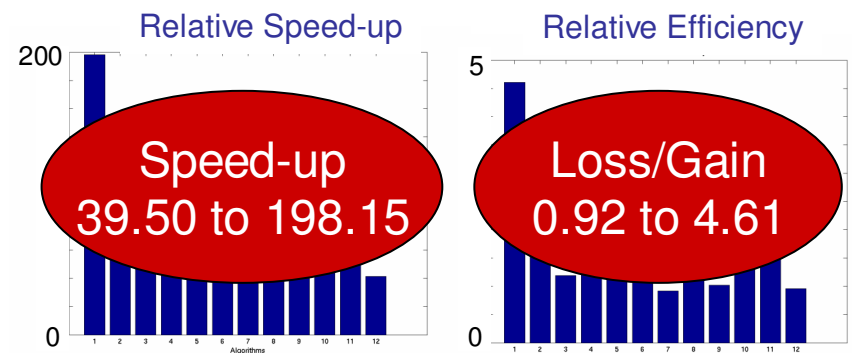
ARM926EJ-S / C-code



C55x / C-code



C64x / C-code

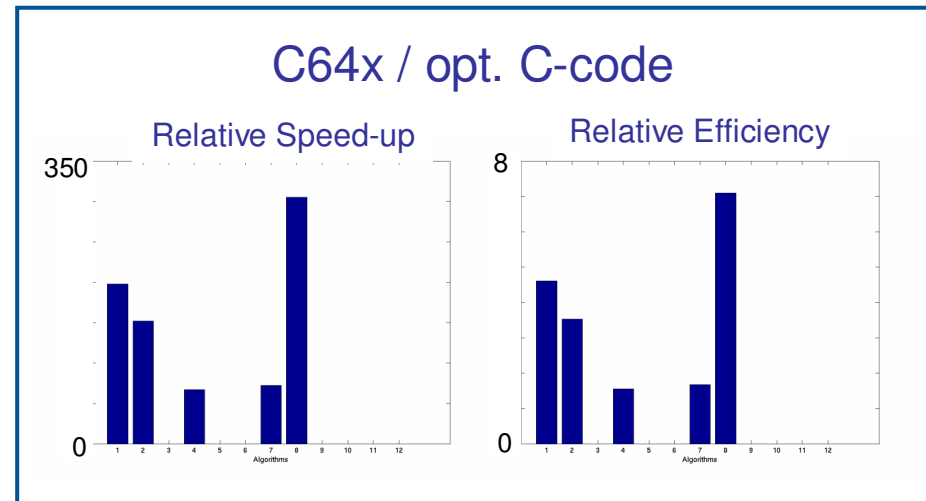
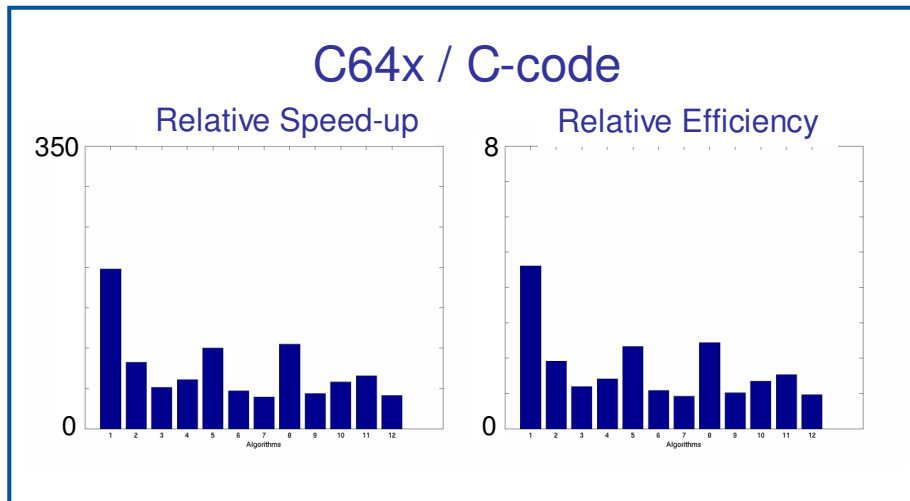


Note: Measurements are normed to ARM720T / C-code implementation

Measurement Results II: optimized C-code investigations

Algorithms:

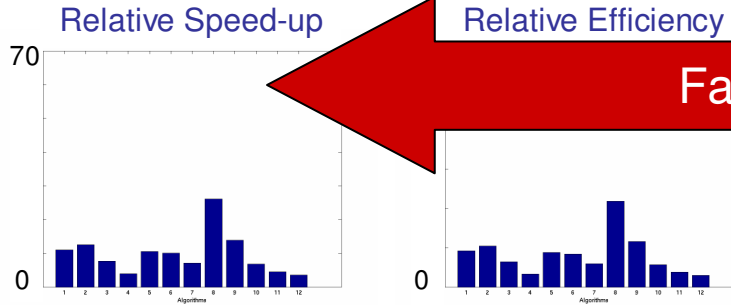
- | | | | |
|---------------------|--------------------------|-------------------------|-----------------------------|
| 1. Vector Addition | 4. Vector Max Index | 7. Matrix Transpose | 10. Complex FIR filter |
| 2. Vector Product | 5. Vector Sum Square | 8. Autocorrelation | 11. Adaptive LMS FIR filter |
| 3. Vector Max Value | 6. Matrix Multiplication | 9. FIR filter (generic) | 12. FFT (Radix-2) |



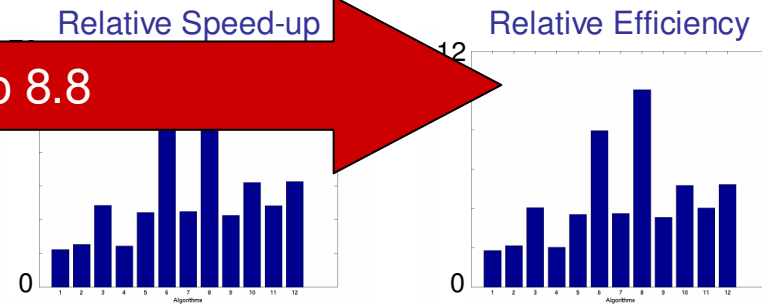
Factor: 1 to ~3

Measurement Results III: Assembly code investigations

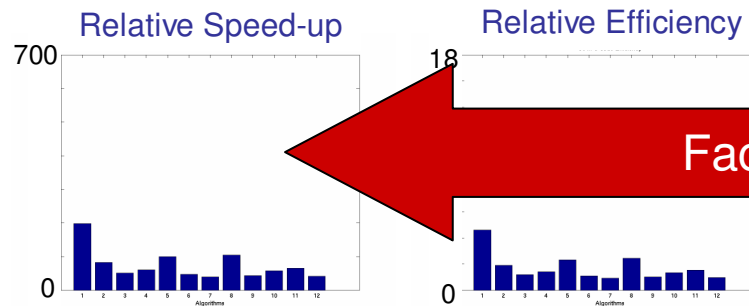
C55x / C-code



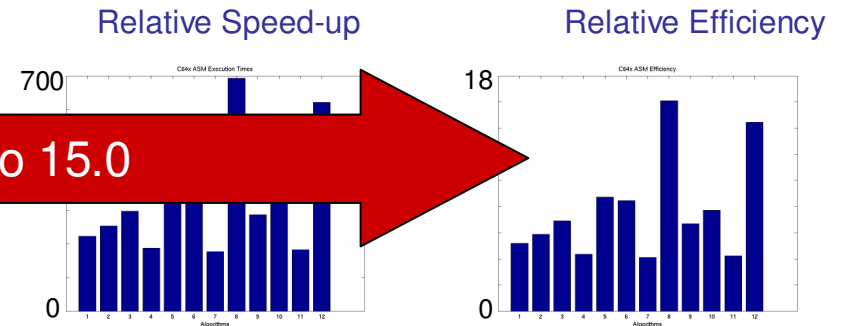
C55x / ASM-code



C64x / C-code



C64x / ASM-code

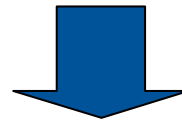


Algorithms:

- | | | | |
|---------------------|--------------------------|-------------------------|-----------------------------|
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Note: Measurements are normed to ARM720T / C-code implementation

- 1. High performance range exists from a “C-code on a DSP” to a “hand-optimized Assembly code on a DSP”.**
- 2. Minor C-code optimizations on basis of compiler directives can improve code generation by the compiler.**
- 3. Typical assumption that: “Assembly programming can be neglected” can not be supported at least not for baseband processing.**



Huge difference in execution time and efficiency forces developers to perform an in depth-analysis of this issue

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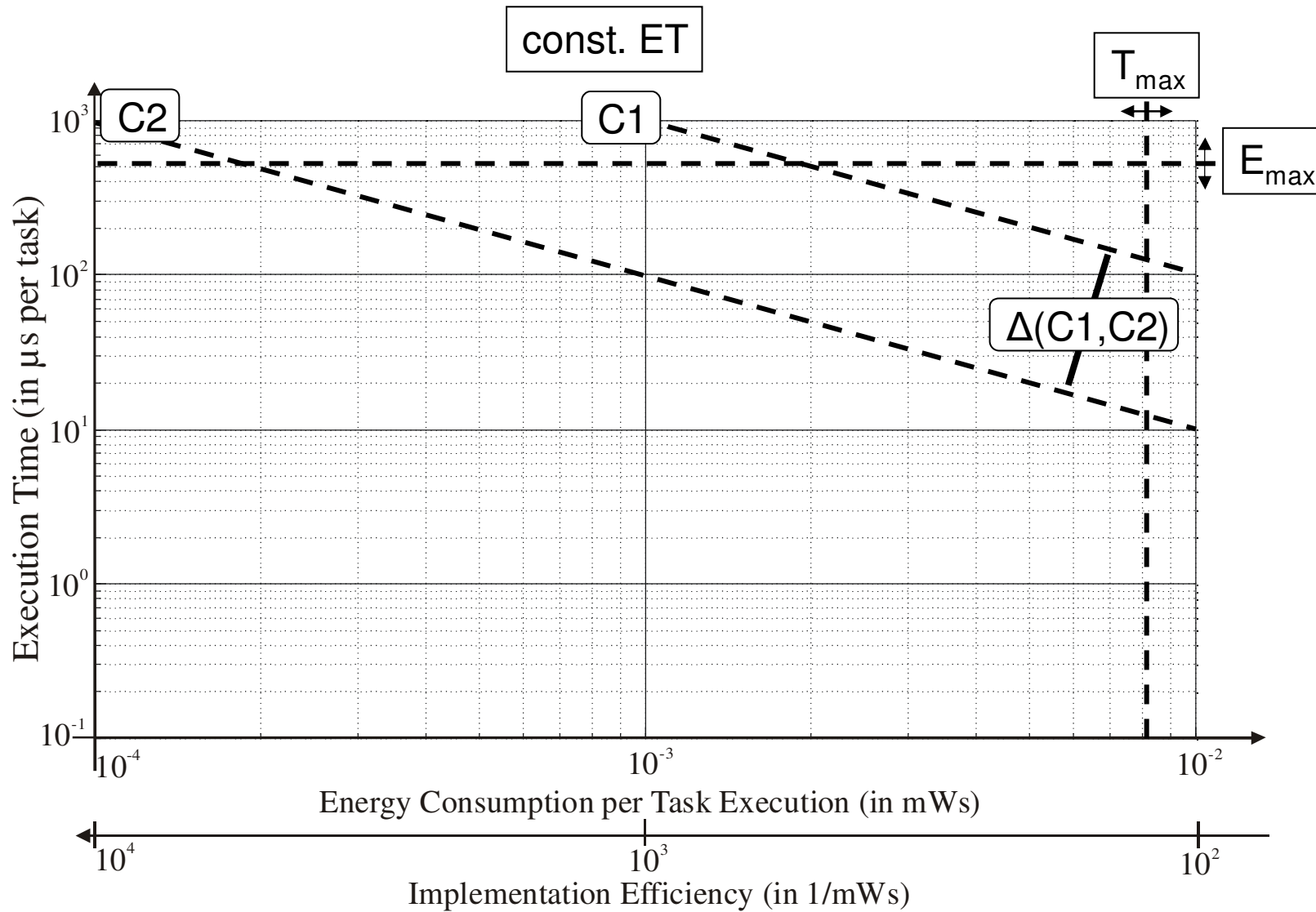
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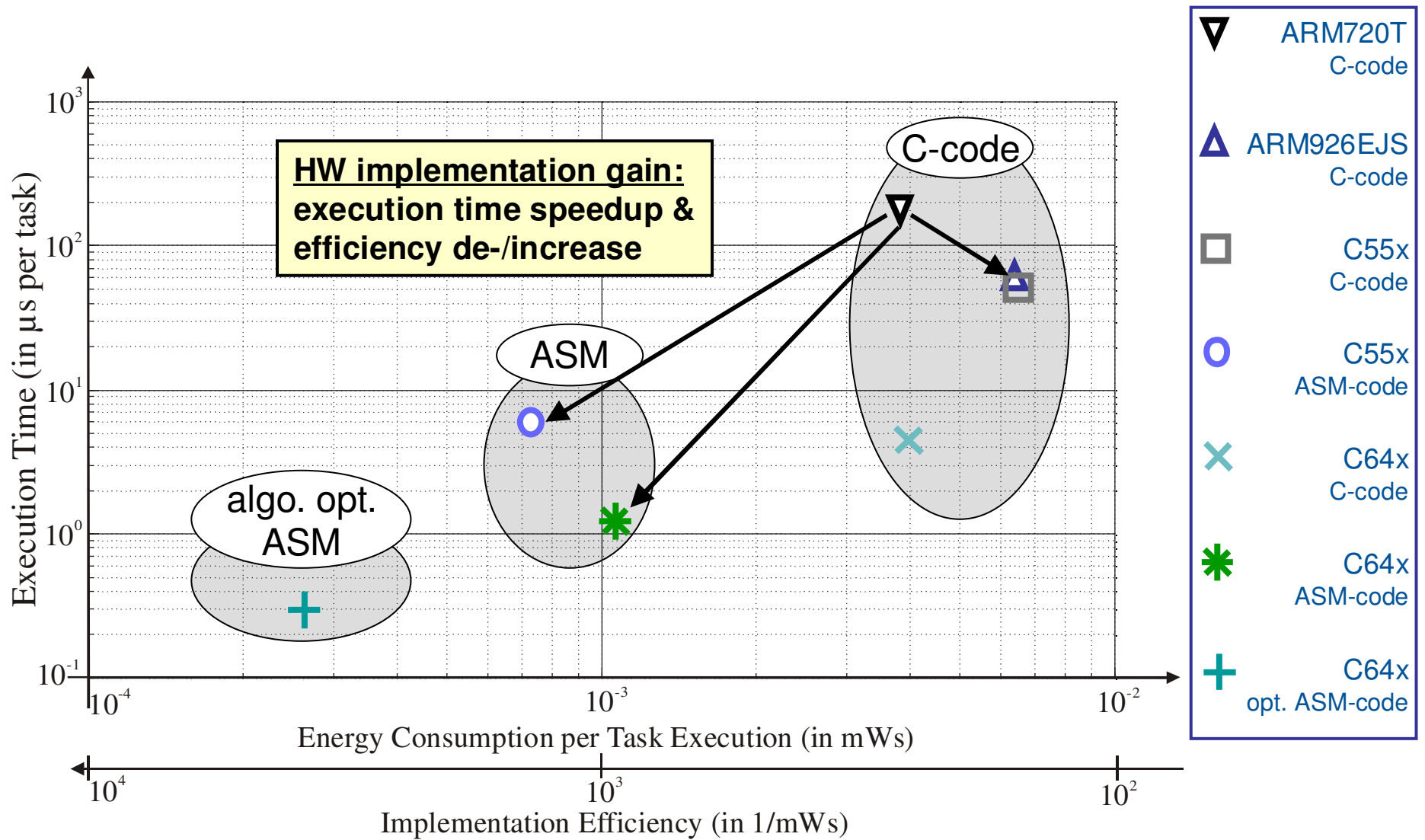
→ In-Depth Analysis: FFT kernel

Conclusion/Outlook

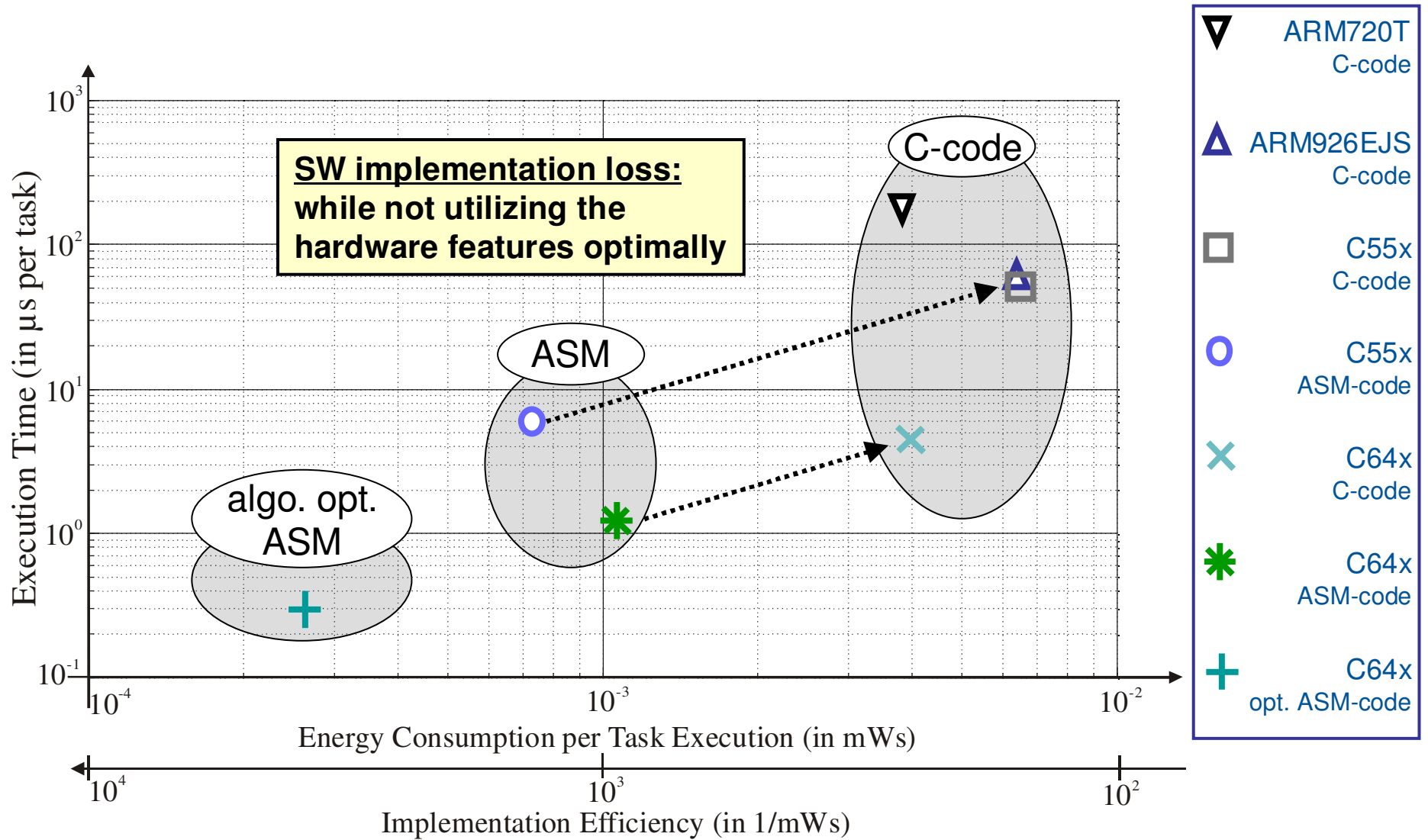
In-Depth Analysis: FFT results



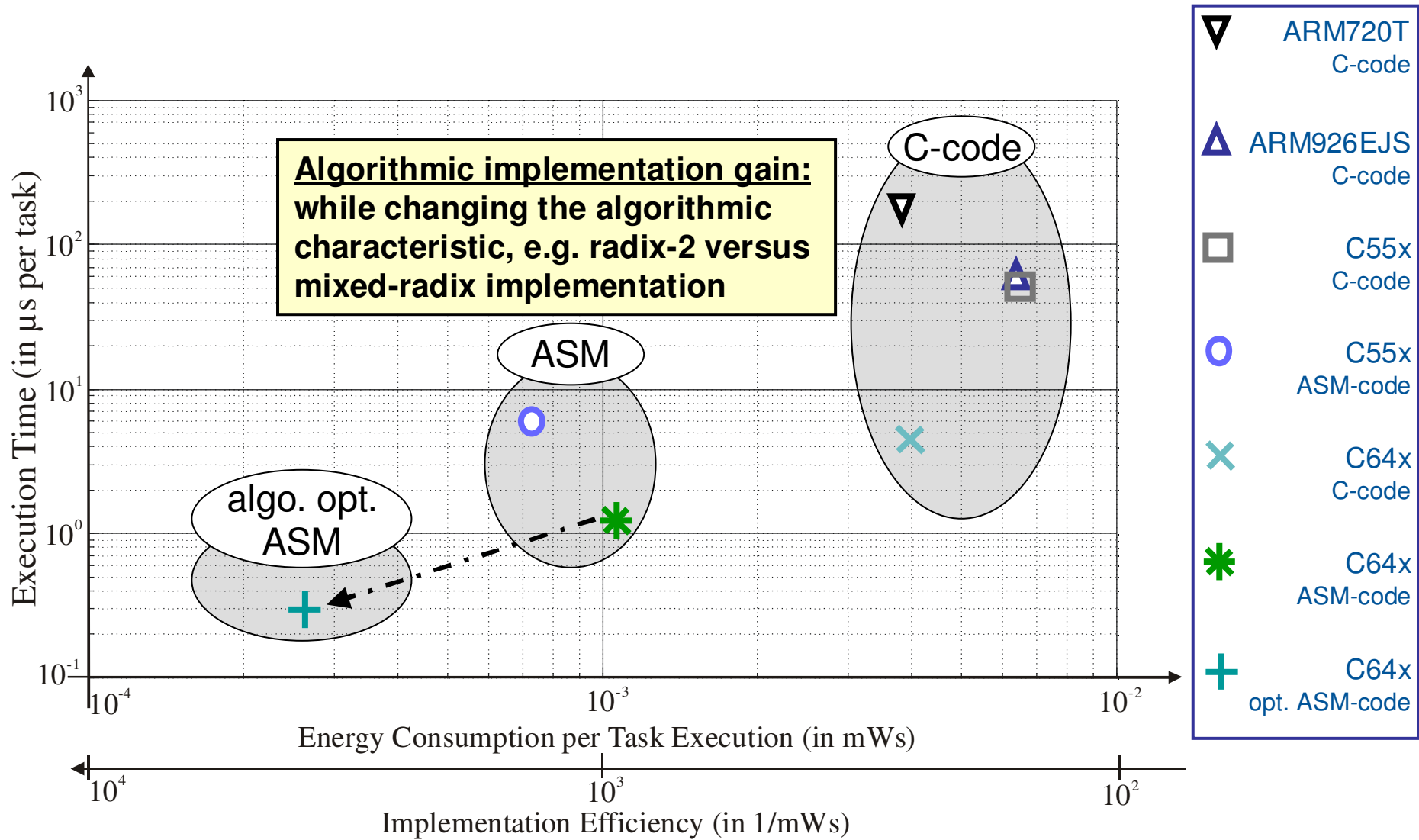
In-Depth Analysis: FFT results



In-Depth Analysis: FFT results



In-Depth Analysis: FFT results





Performance Issues of "portable" C-Code:

- High overhead in general purpose C-code
(*up to one order of magnitude compared to asm.*)

A few well known reasons:

- Missing Support for Semantics
(*Fixed Point Arithmetic, Special Instructions, ...*)
- Missing Memory Architecture Support
- Missing Information on Pointers, Loop Counters, ...
(*Compilers must be functionally correct in worst case*)

ARM720T
C-code

T926EJS
C-code

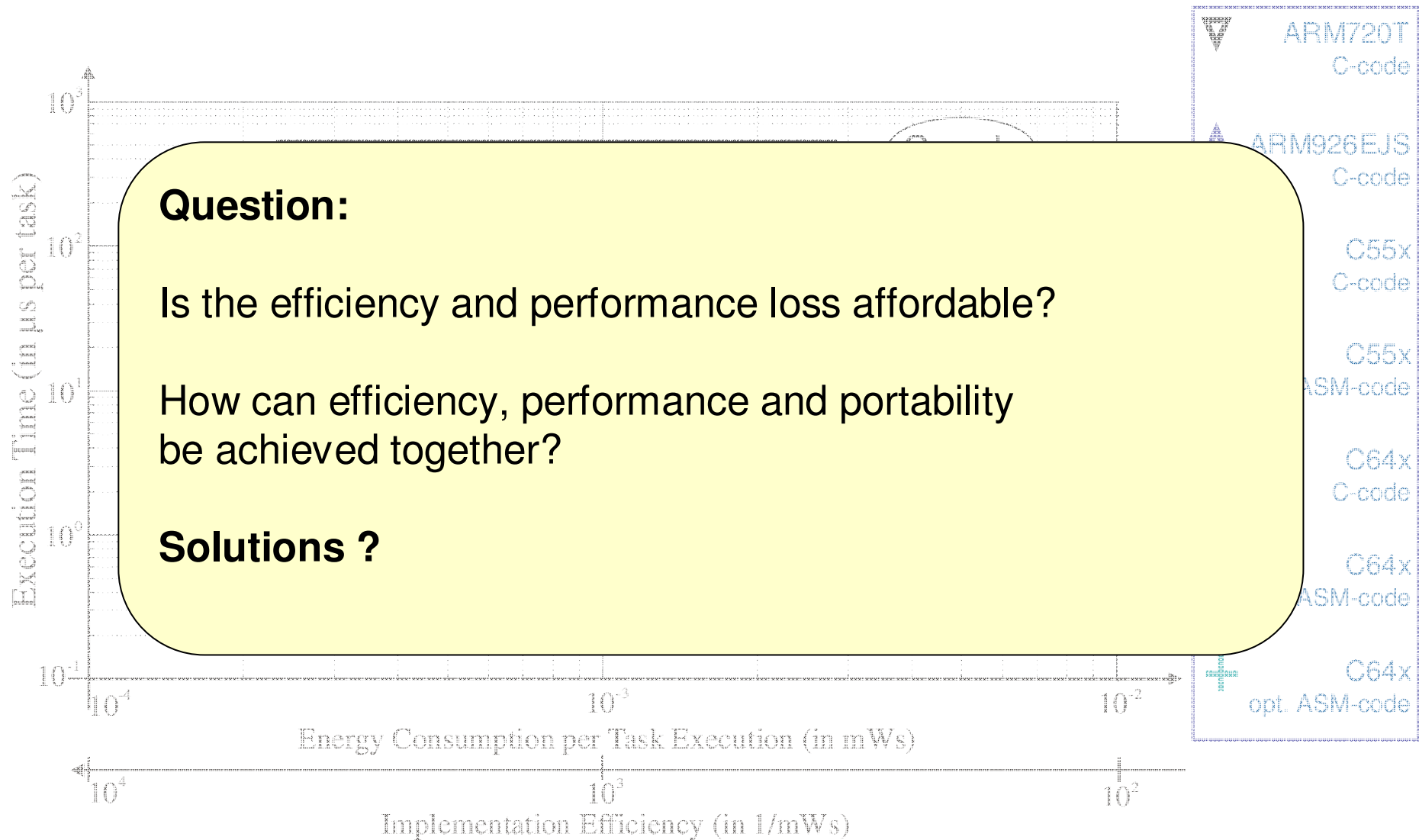
C55x
C-code

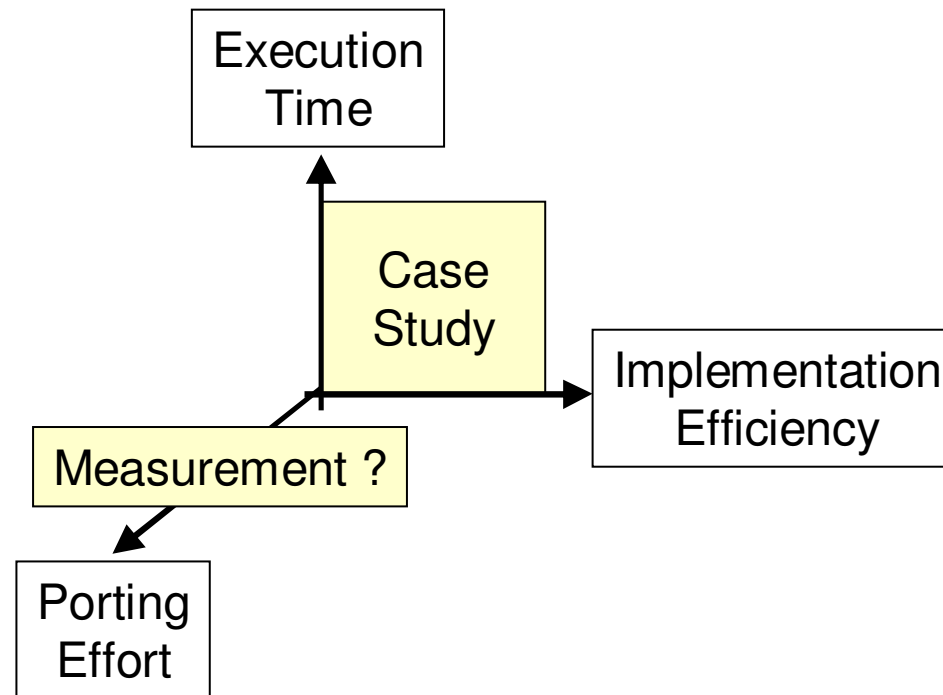
C55x
ASM-code

C64x
C-code

C64x
ASM-code

C64x
ASM-code





- **Measurement of Porting Effort is rather difficult, since development time depends heavily on developer (trainee vs. experienced developer)**
 - E.g. development of an optimized FIR filter in Assembly on the ARM Cortex-R4 is reported to be approx. 20 hours [1]

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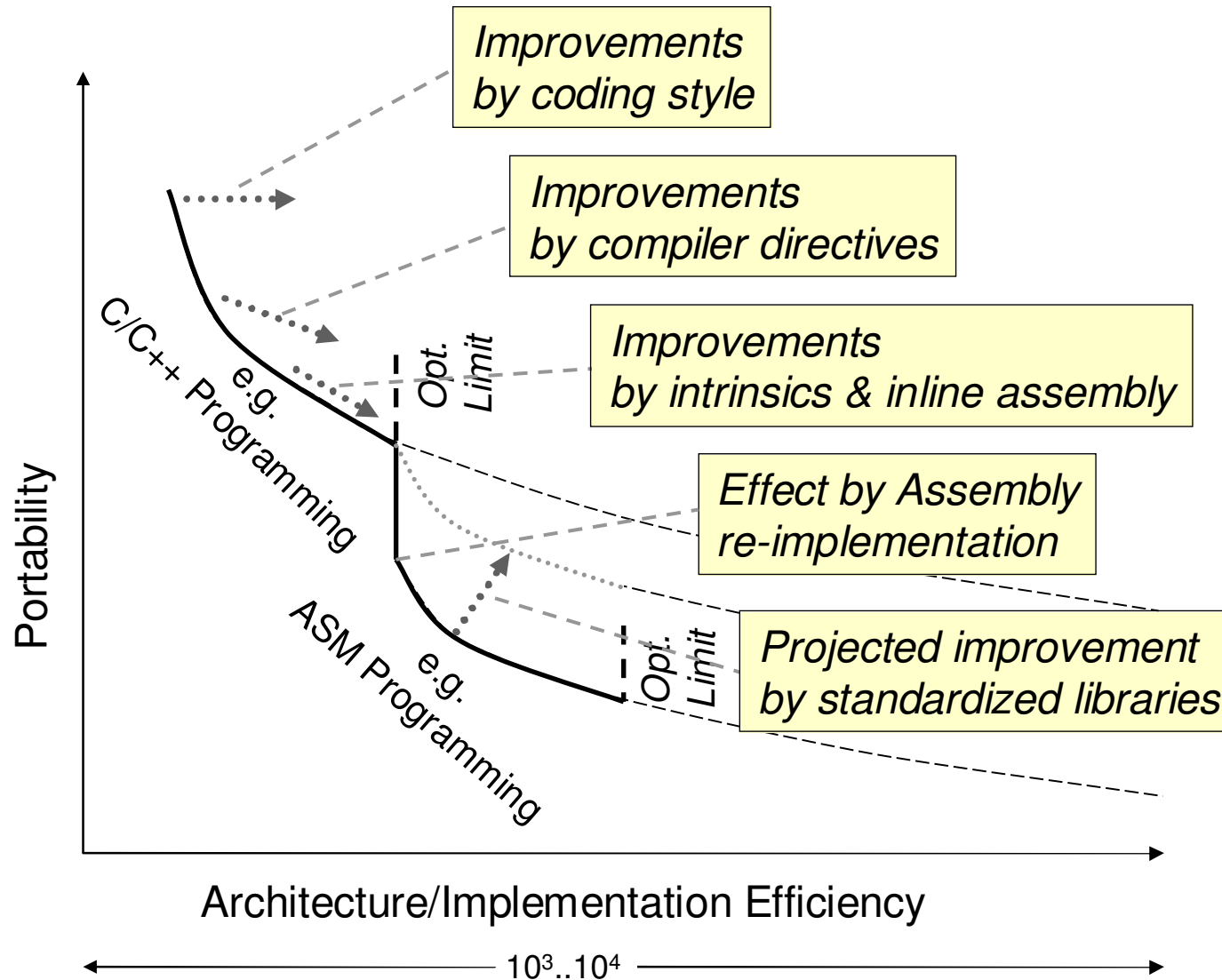
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Conclusions: Projected Effects of Optimizations



Summary:

- Case study investigating portability vs. efficiency has been presented
- Three key effects have been discussed
 - HW implementation gain
 - SW implementation loss
 - Algorithmic implementation gain
- Possible optimizations to increase portability along with efficiency have been highlighted

Outlook:

- Further investigation of portability versus efficiency
- Waveform Description Languages (WDLs) on basis of such standardized libraries



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Questions?

Thank you

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